

N-Channel Enhancement Mode MOSFET

TDM2348

DESCRIPTION

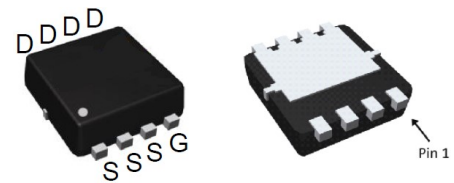
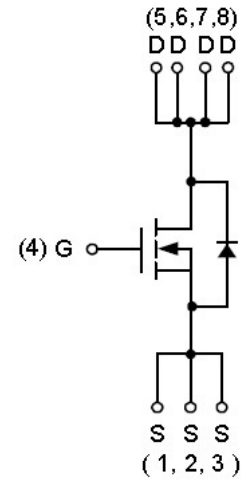
The TDM2348 uses advanced technology to provide excellent $R_{DS(ON)}$ and low gate charge. This device is suitable for use as a load switch or in PWM applications.

GENERAL FEATURES

- 30V/80A
 $R_{DS(ON)} < 5m\Omega$ @ $V_{GS}=10V$
 $R_{DS(ON)} < 9m\Omega$ @ $V_{GS}=4.5V$
- Low $R_{DS(on)}$ & FOM
- Extremely low switching loss
- Excellent stability and uniformity
- Lead free product is available
- Surface Mount Package

Application

- High Frequency Switching.
- Synchronous Rectification.



PPAK-3*3-8

ABSOLUTE MAXIMUM RATINGS($T_c=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current @ Current-Pulsed note1	I_{DM} ($T_c=25^\circ\text{C}$)	320	A
Drain Current @ Continuous note2	I_D ($T_c=25^\circ\text{C}$)	80	A
	I_D ($T_c=100^\circ\text{C}$)	45	A
Power Dissipation note3	P_D ($T_c=25^\circ\text{C}$)	70	W
Avalanche Energy, Single pulse note1, 4	$E_{AS}(L=0.5mH)$	306	mJ

THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to-Case note5	$R_{\theta JC}$	2.5	$^\circ\text{C}/\text{W}$
Operating Junction Temperature Range	T_J	-55 To 150	$^\circ\text{C}$
Operating Junction and Storage Temperature Range	T_{STG}	-55 To 150	$^\circ\text{C}$

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ELECTRICAL CHARACTERISTICS ($T_J=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	30	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V, V _{GS} =0V	-	-	1	μ A
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	-	± 100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	1.0	1.6	2.2	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =30A	-	3.9	5.0	mΩ
		V _{GS} =4.5V, I _D =20A	-	6.8	9.0	mΩ
DYNAMIC CHARACTERISTICS						
Input Capacitance	C _{iss}	V _{DS} =15V, V _{GS} =0V, F=1.0MHz	-	1970	-	PF
Output Capacitance	C _{oss}		-	215	-	PF
Reverse Transfer Capacitance	C _{rss}		-	178	-	PF
SWITCHING CHARACTERISTICS						
Turn-on Delay Time	t _{d(on)}	V _{DS} =15V, V _{GS} =10V, R _G =2.7Ω, I _D =30A	-	20	-	nS
Turn-on Rise Time	t _r		-	15	-	nS
Turn-Off Delay Time	t _{d(off)}		-	60	-	nS
Turn-Off Fall Time	t _f		-	10	-	nS
Total Gate Charge	Q _g	V _{DS} =15V, I _D =30A, V _{GS} =10V	-	37.3	-	nC
Gate-Source Charge	Q _{gs}		-	5.8	-	nC
Gate-Drain Charge	Q _{gd}		-	7.7	-	nC
DIODE CHARACTERISTICS						
Continuous Source Current	I _s		-	-	80	A
Maximum Pulsed Drain-Source Diode Forward Current	I _{SM}		-	-	320	A
Diode Forward Voltage	V _{sD}	V _{GS} =0V, I _s =30A	-	-	1.2	V
Reverse recovery time, I _F =30A dI/dt=100A/μs	T _{rr}	T _J =25°C, I _F =80A, di/dt= 100A/μs	-	32	-	ns
Reverse recovery charge, I _F =30A dIF/dt=100A//μs	Q _{rr}	T _J =25°C, I _F =80A, di/dt= 100A/μs	-	12	-	nC

Notes:

- Single pulse width limited by junction temperature $T_{J(MAX)}=150^{\circ}C$.
- The maximum current rating is package limited.
- The power dissipation P_D is based on $T_{J(MAX)}=150^{\circ}C$, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.
- The EAS data shows Max. rating. The test condition is $V_{DD}=15V$, $V_{GS}=10V$, $L=0.5mH$.
- The value of $R_{\theta JC}$ is measured in a still air environment with $T_A=25^{\circ}C$ and the maximum allowed junction temperature of $150^{\circ}C$. The value in any given application depends on the user's specific board design.

Typical Operating Characteristics

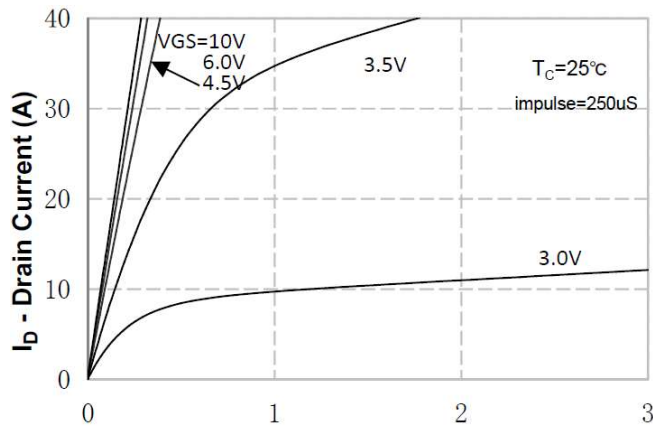


Figure 1. On-Region Characteristics

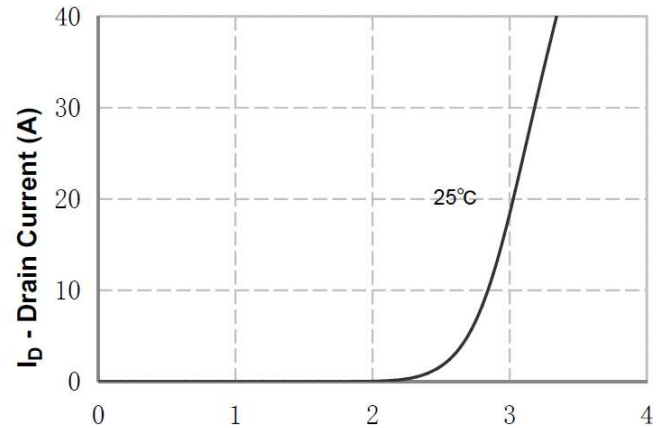


Figure 2. Transfer Characteristics

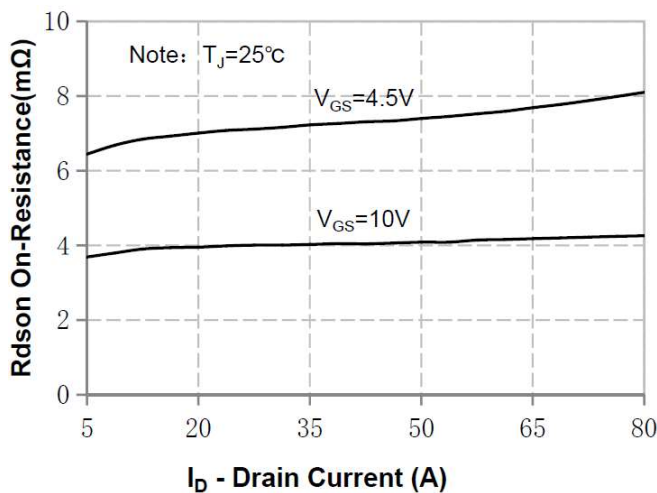


Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage

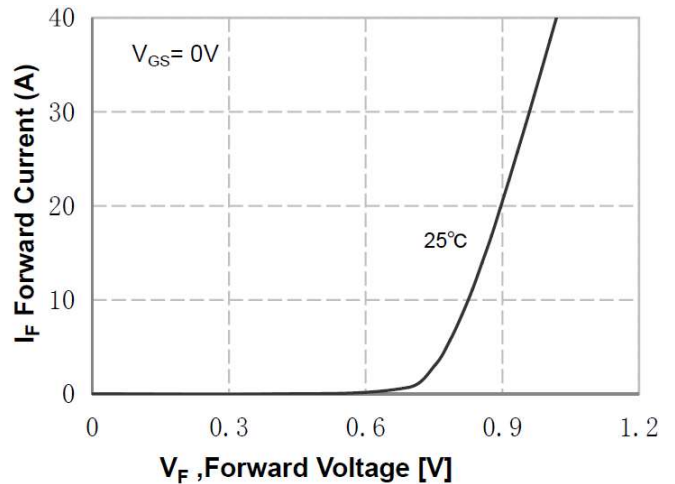


Figure 4. Body Diode Forward Voltage Variation vs Source Current

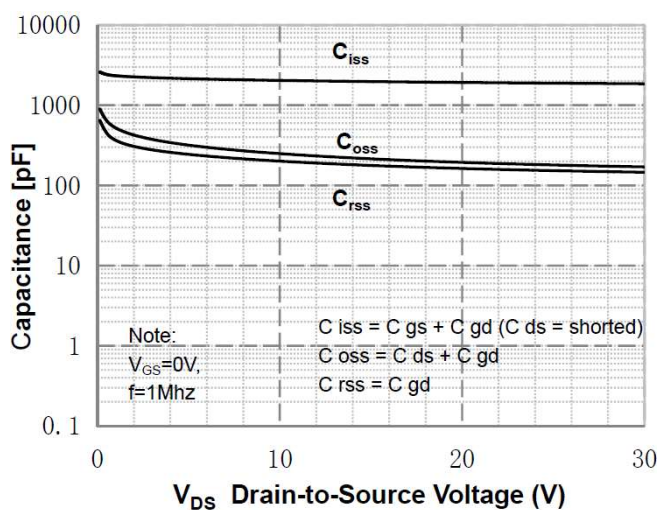


Figure 5. Capacitance Characteristics

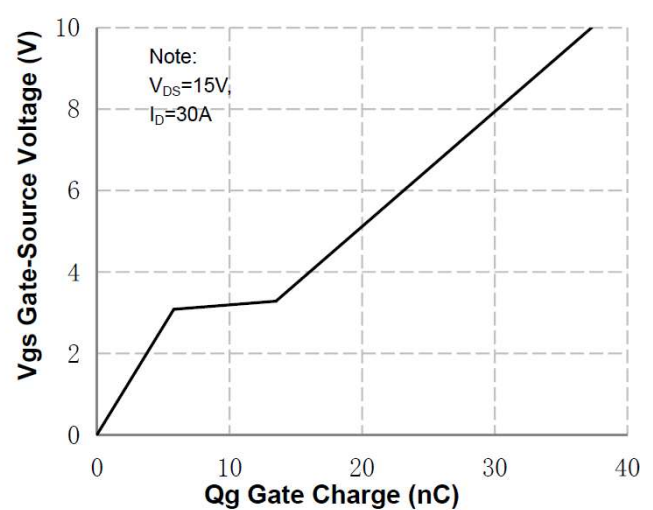


Figure 6. Gate Charge Characteristics

Typical Operating Characteristics(Cont.)

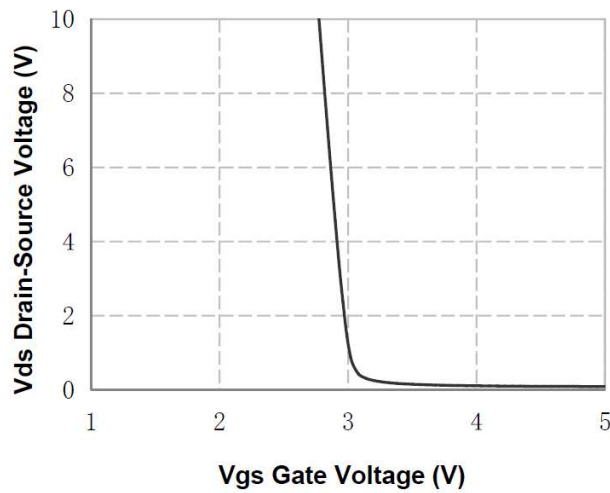


Figure 7. Vds Drain-Source Voltage vs Gate Voltage

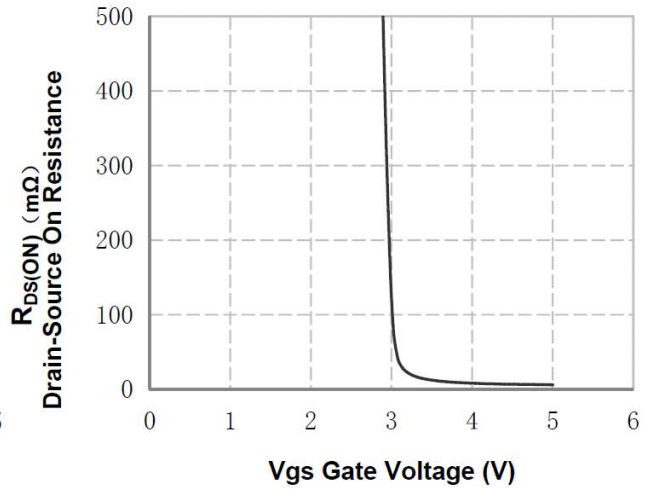


Figure 8. On-Resistance vs Gate Voltage

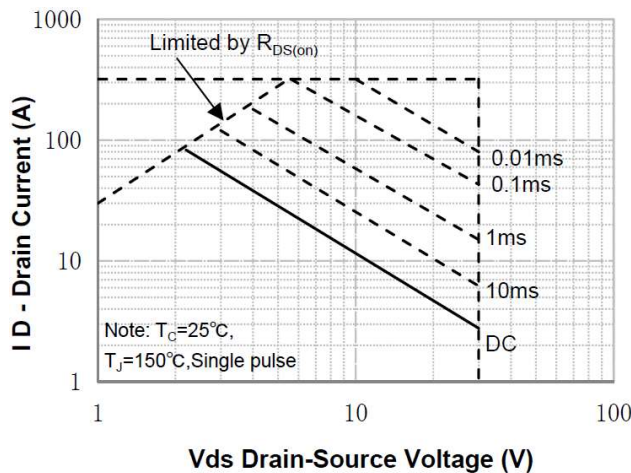


Figure 9. Maximum Safe Operating Area

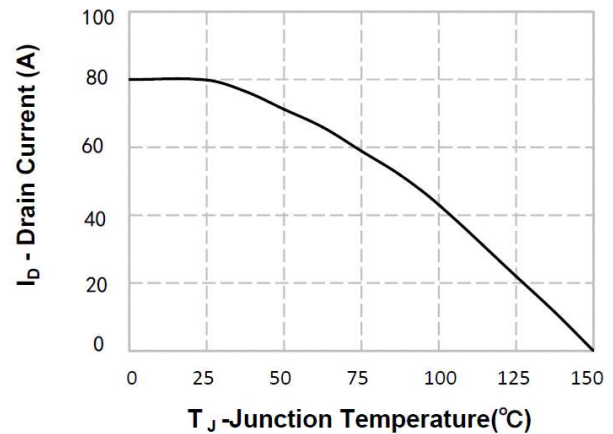


Figure 10. Maximum Continuous Drain Current vs Temperature

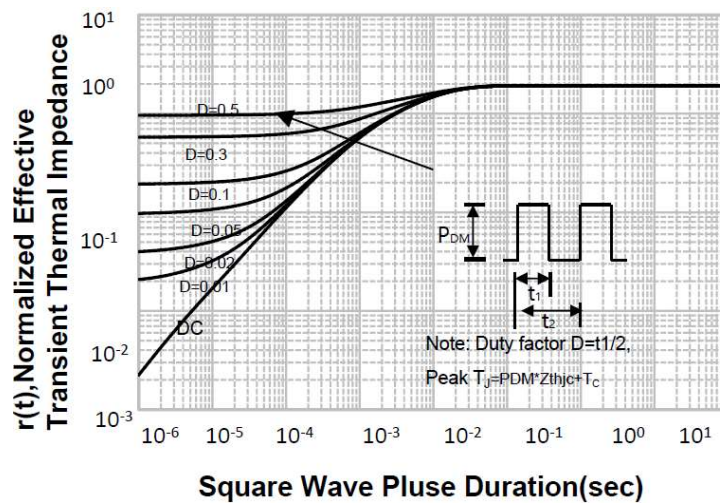


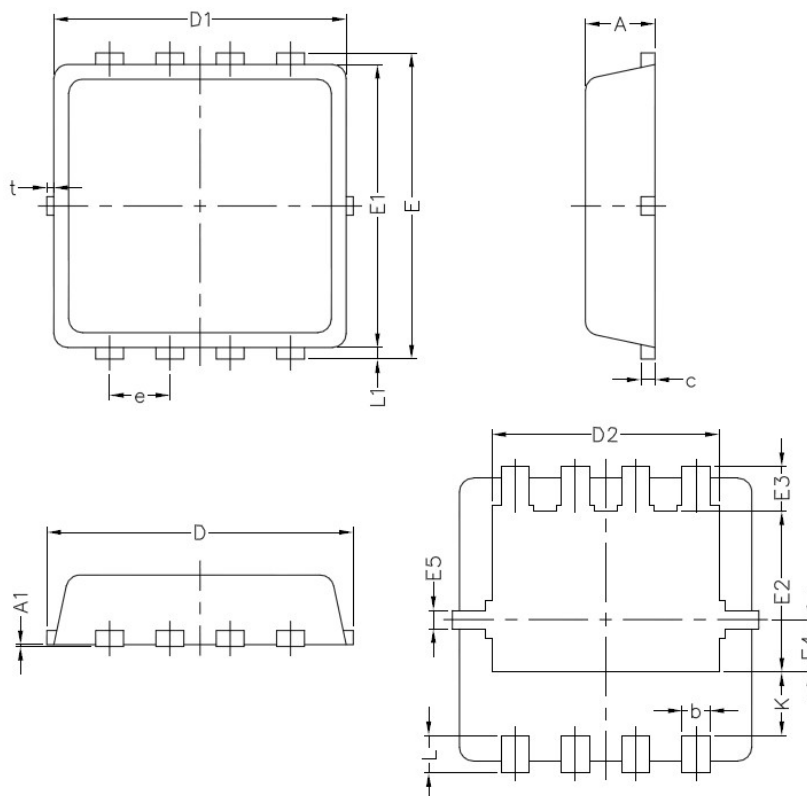
Figure 11. Transient Thermal Response Curve

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Package Information

PPAK-3*3-8 Package



Symbol	PPAK-3*3-8(mm)		
	Min	Nom	Max
A	0.70	0.75	0.85
A1	/	/	0.05
b	0.20	0.30	0.40
c	0.10	0.152	0.25
D	3.15	3.3	3.45
D1	3.00	3.15	3.30
D2	2.25	2.45	2.65
E	3.15	3.30	3.45
E1	2.90	3.05	3.20
E2	1.54	1.74	1.94
E3	0.28	0.48	0.68
E4	0.37	0.57	0.77
E5	0.10	0.20	0.30
e	0.60	0.65	0.70
K	0.49	0.69	0.89
L	0.30	0.40	0.50
L1	0.06	0.125	0.20
t	/	/	0.13

Design Notes