

### Features

- -20V/-1.5A ,  
 $R_{DS(ON)} = 130m\Omega(\text{typ.}) @ V_{GS} = -4.5V$   
 $R_{DS(ON)} = 170m\Omega(\text{typ.}) @ V_{GS} = -2.5V$
- Super High Dense Cell Design
- Reliable and Rugged
- Lead Free Available (RoHS Compliant)

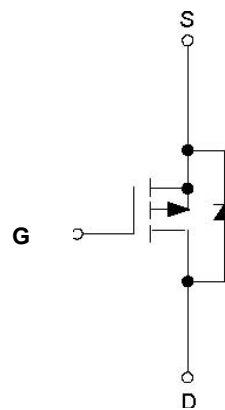
### Applications

- Power Management in Notebook Computer , Portable Equipment and Battery Powered Systems.

### Pin Description



Top View of SOT23-3L



P-Channel MOSFET

### Ordering and Marking Information

<p>TDM3415S□ □ □ □</p> <ul style="list-style-type: none"> <li>□ — Lead Free Code</li> <li>□ — Handing Code</li> <li>□ — Temp. Range</li> <li>□ — Package Code</li> </ul>	<p>Package Code  A: SOT23-3L</p> <p>Operating Junction Temp. Rang  C: -55 to 150°C</p> <p>Handing Code  TU:Tube TR:Tape &amp; Reel</p> <p>Lead Free Code:  L:Lead Free Device Blank:Original Device</p>
<p>TDM3415S M23 X</p>	<p>X:Date Code</p>

**Note:** TECHCODE lead-free products contain molding compounds/die attach materials and 100% matte in plate termination finish; which are fully compliant with RoHS and compatible with both SnPb and lead-free soldering operations. TECHCODE lead-free products meet or exceed the lead-free requirements of IPC/JEDEC J STD-020C for MSL classification at lead-free peak reflow temperature.

TECHCODE reserves the right to make changes to improve reliability or manufacturability without notice, and advise customers to obtain the latest version of relevant information to verify before placing orders.

### Absolute Maximum Ratings ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter		Rating	Unit
VDSS	Drain-Source Voltage		-20	V
VGSS	Gate-Source Voltage		$\pm 12$	
ID*	Continuous Drain Current		-1.5	A
IDM*	300 $\mu\text{s}$ Pulsed Drain Current	VGS=-4.5V	-6	
IS*	Diode Continuous Forward Current		-1	A
TJ	Maximum Junction Temperature		150	$^\circ\text{C}$
TSTG	Storage Temperature Range		-55 to 150	
PD*	Maximum Power Dissipation	TA=25 $^\circ\text{C}$	0.83	W
		TA=100 $^\circ\text{C}$	0.3	
R $\theta$ JA*	Thermal Resistance-Junction to Ambient		150	$^\circ\text{C}/\text{W}$

Note: \*Surface Mounted on 1in<sup>2</sup> pad area, t  $\leq$  10sec.

### Electrical Characteristics ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Condition	TDM3415S			Unit
			Min.	Typ.	Max.	
<b>Static Characteristics</b>						
BVDSS	Drain-Source Breakdown Voltage	VGS=0V, ID=-250 $\mu\text{A}$	-20			V
IDSS	Zero Gate Voltage Drain Current	VDS=-16V, VGS=0V TJ=85 $^\circ\text{C}$			-1 -30	$\mu\text{A}$
VGS(th)	Gate Threshold Voltage	VDS=VGS, ID=-250 $\mu\text{A}$	-0.5	-0.6	-1	V
IGSS	Gate Leakage Current	VGS= $\pm 12\text{V}$ , VDS=0V			$\pm 100$	nA
RDS(ON) a	Drain-Source On-state Resistance	VGS=-4.5V, ID=-1.5A		130	170	m $\Omega$
		VGS=-2.5V, ID=-0.6A		170	220	
VSD a	Diode Forward Voltage	ISD=-1A, VGS=0V		-0.7	-1.3	V
<b>Gate Charge Characteristics <sup>b</sup></b>						
Qg	Total Gate Charge	VDS=-10V, VGS=-4.5V, ID=-1A		5	7	
Qgs	Gate-Source Charge			1.3		nC
Qgd	Gate-Drain Charge			1.3		

Electrical Characteristics (Cont.) ( $T_A = 25^\circ\text{C}$  unless otherwise noted)

Symbol	Parameter	Test Condition	TDM3415S			Unit
			Min.	Typ.	Max.	
<b>Dynamic Characteristics<sup>b</sup></b>						
R <sub>G</sub>	Gate Resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, F=1MHz		12		Ω
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> =0V, V <sub>DS</sub> =-15V, Frequency=1.0MHz		350		PF
C <sub>oss</sub>	Output Capacitance			75		
C <sub>rss</sub>	Reverse Transfer Capacitance			55		
t <sub>d(ON)</sub>	Turn-on Delay Time	V <sub>DD</sub> =-10V, R <sub>L</sub> =10Ω, I <sub>DS</sub> =-1A, V <sub>GEN</sub> =-4.5V, R <sub>G</sub> =6Ω		6	10	Ns
T <sub>r</sub>	Turn-on Rise Time			8	12	
t <sub>d(OFF)</sub>	Turn-off Delay Time			25	45	
T <sub>f</sub>	Turn-off Fall Time			20	36	

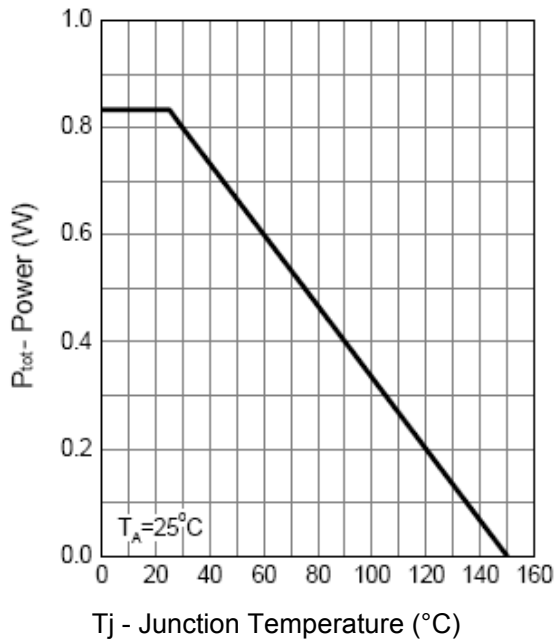
Notes:

a : Pulse test ; pulse width ≤ 300μs, duty cycle ≤ 2%.

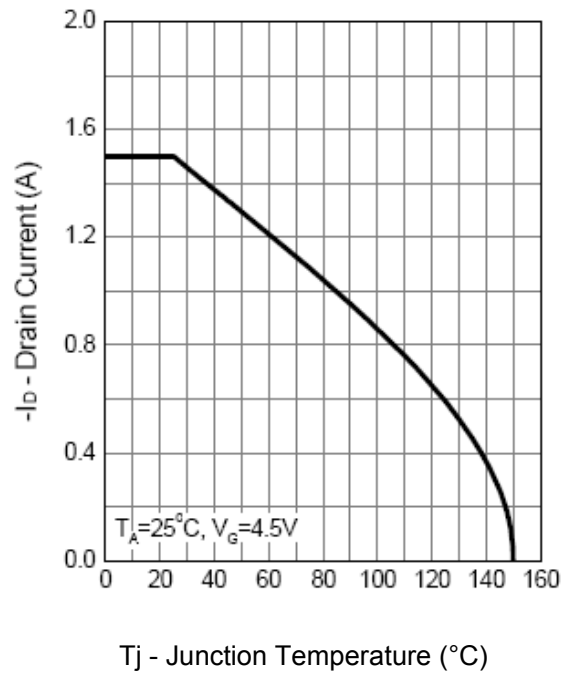
b : Guaranteed by design, not subject to production testing.

### Typical Characteristics

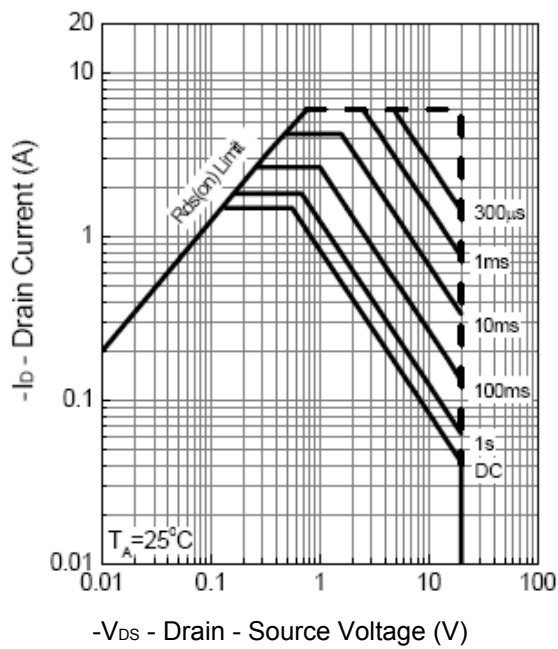
#### Power Dissipation



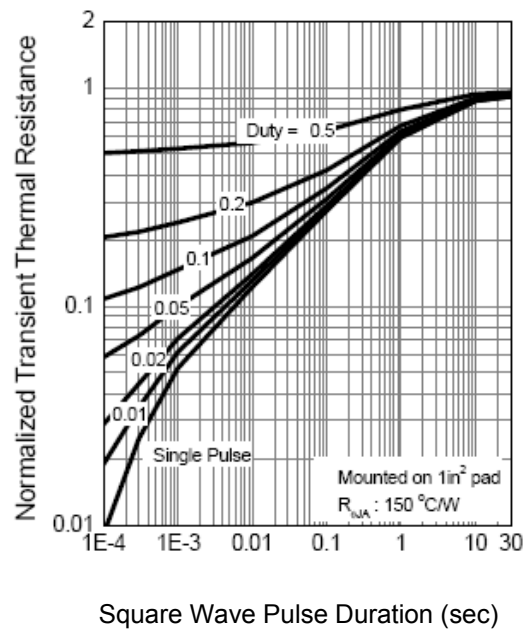
#### Drain Current



#### Safe Operation Area

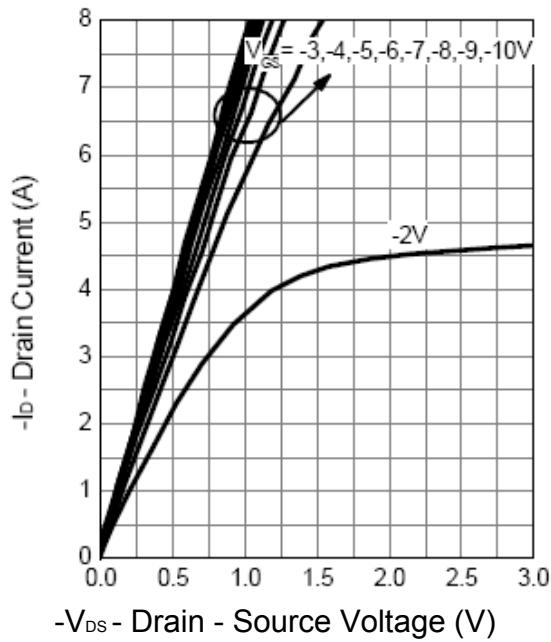


#### Thermal Transient Impedance

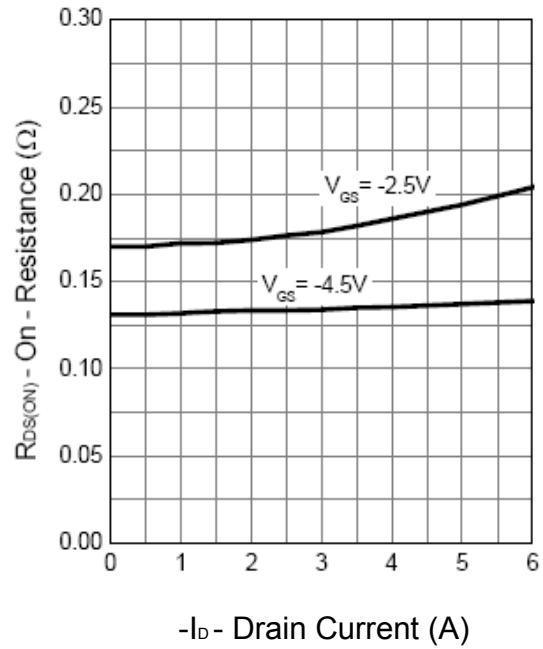


Typical Characteristics (Cont.)

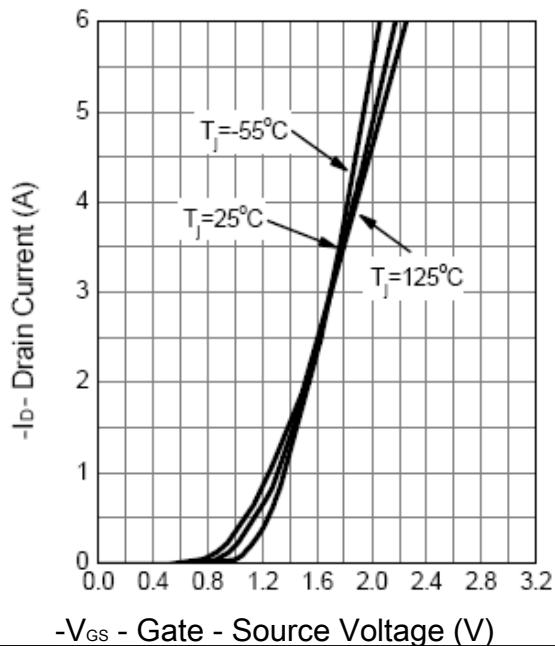
Output Characteristics



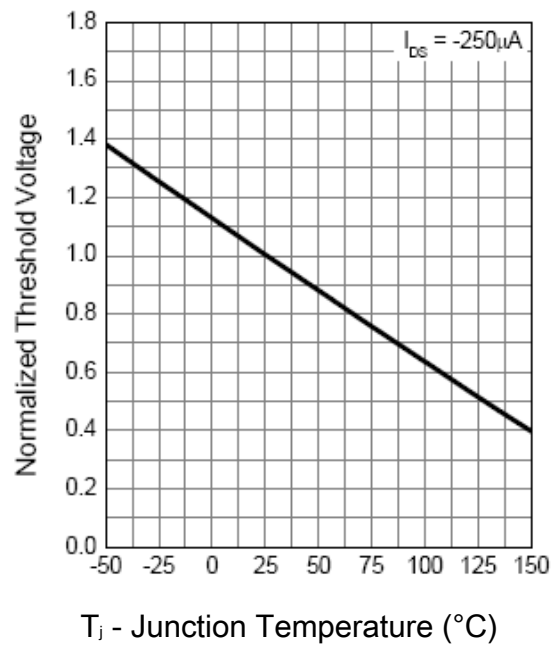
Drain-Source On Resistance



Transfer Characteristics

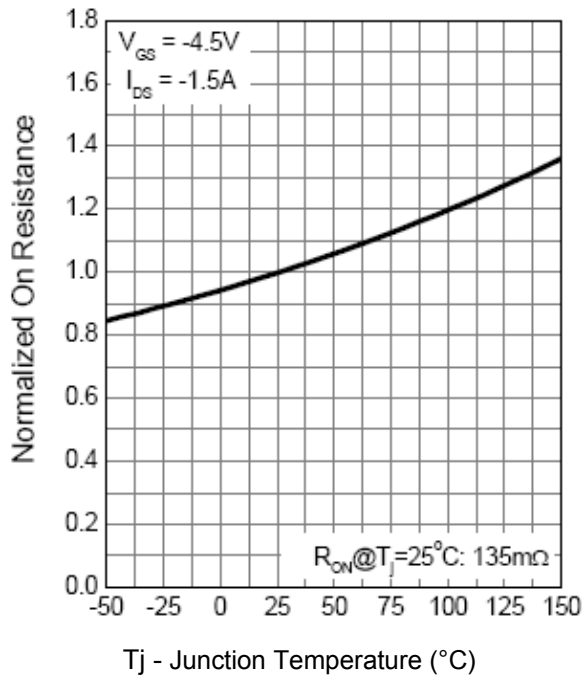


Gate Threshold Voltage

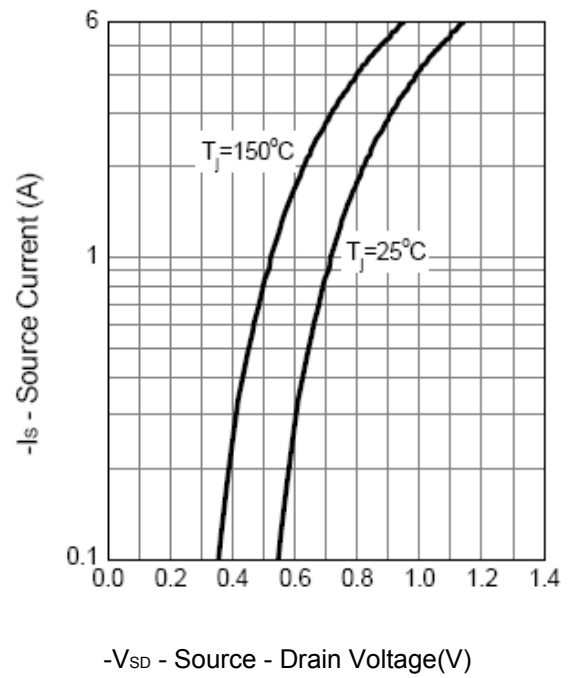


Typical Characteristics (Cont.)

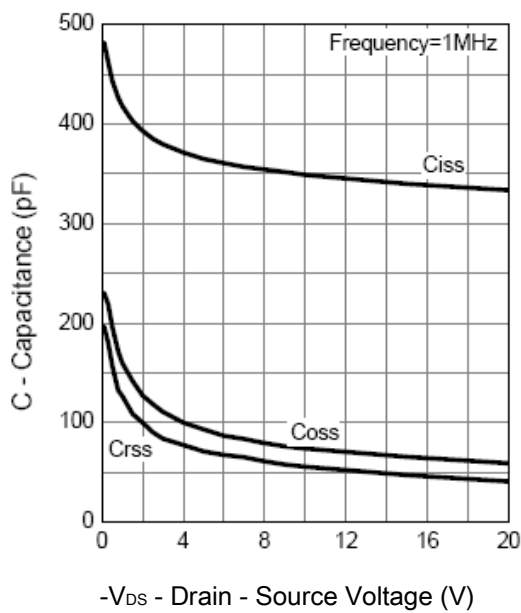
Drain-Source On Resistance



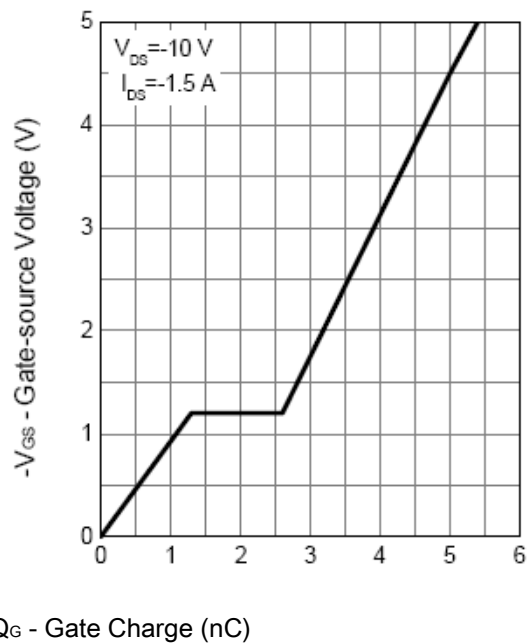
Source-Drain Diode Forward



Capacitance

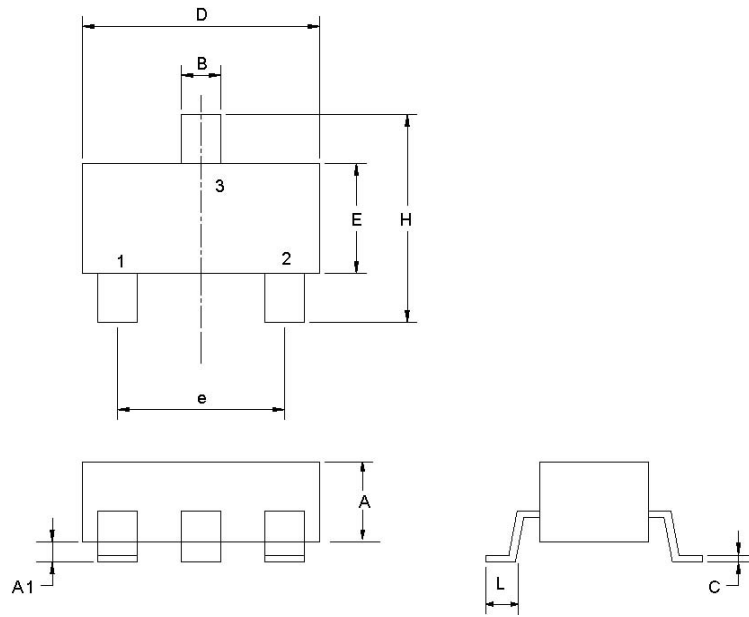


Gate Charge



Packaging Information

SOT23-3L

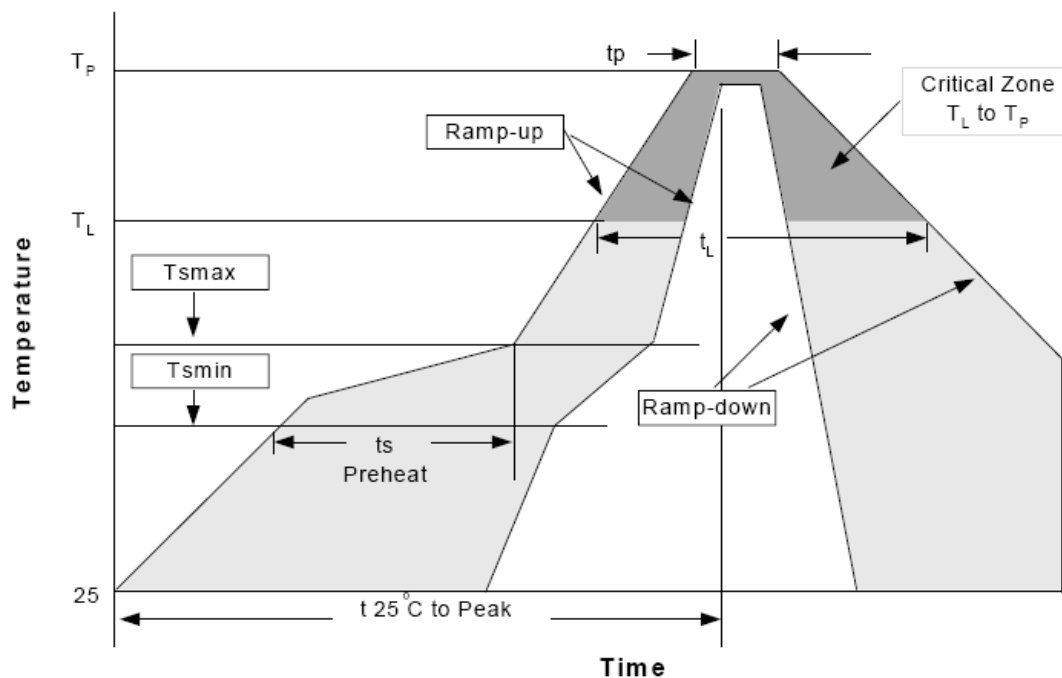


Dim	Millimeters		Inches	
	Min.	Max.	Min.	Max.
A	1.00	1.30	0.039	0.051
A1	0.00	0.10	0.000	0.004
B	0.35	0.51	0.014	0.020
C	0.10	0.25	0.004	0.010
D	2.70	3.10	0.106	0.122
E	1.40	1.80	0.055	0.071
e	1.90/2.1 BSC.		0.075/0.083 BSC.	
H	2.40	3.00	0.094	0.118
L	0.37		0.015	

### Physical Specifications

Terminal Material	Solder-Plated Copper (Solder Material : 90/10 or 63/37 SnPb), 100%Sn
Lead Solderability	Meets EIA Specification RSI86-91, ANSI/J-STD-002 Category 3.

### Reflow Condition (IR/Convection or VPR Reflow)



### Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly		Pb-Free Assembly	
	Large Body	Small Body	Large Body	Small Body
Average ramp-up rate (TL to TP)	3°C/second max.		3°C/second max.	
Preheat	100°C		150°C	
Temperature Min (Tsmain)	150°C		200°C	
Temperature Max (Tsmax)	60-120 seconds		60-180 seconds	
Time (min to max) (ts)	183°C		217°C	
Time maintained above:	60-150 seconds		60-150 seconds	
Temperature (TL)	225+0/-5°C		240+0/-5°C	245+0/-5°C
Time (tL)	10-30 seconds		20-40 seconds	
Peak/Classification Temperature (Tp)	6°C/second max		6°C/second max	
Time within 5°C of actual Peak Temperature (tp)	6 minutes max		8 minutes max	
Ramp-down Rate				
Time 25°C to Peak Temperature				

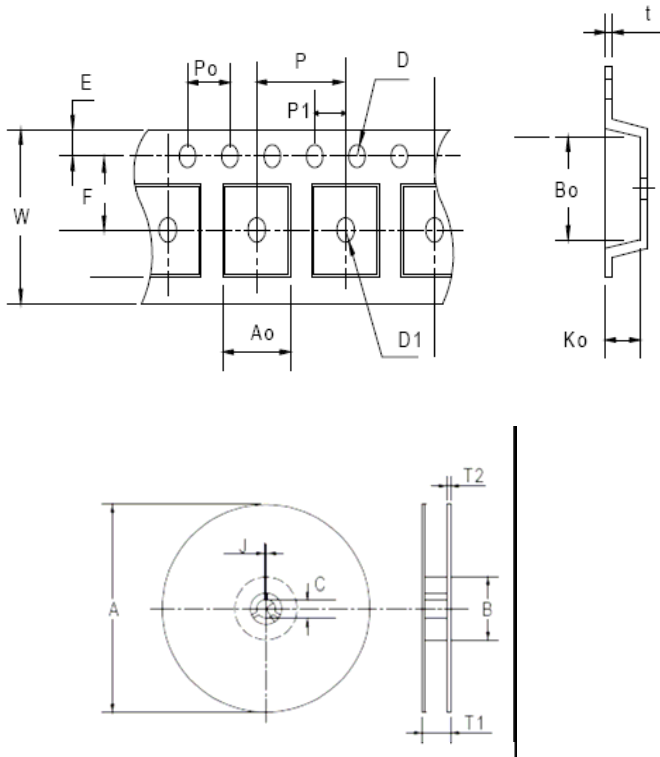
Notes: All temperatures refer to topside of the package .Measured on the body surface.



### Reliability Test Program

Test item	Method	Description
SOLDERABILITY	MIL-STD-883D-2003	245°C, 5 SEC
HOLT	MIL-STD 883D-1005.7	1000 Hrs Bias @ 125°C
PCT	JESD-22-B, A102	168 Hrs, 100% RH, 121°C
TST	MIL-STD 883D-1011.9	-65°C ~ 150°C, 200 Cycles

### Carrier Tape & Reel Dimensions



Application	A	B	C	J	T1	T2	W	P	E
SOT23-3L	178±1	60 ± 1.0	12.0	2.5 ± 0.15	9.0 ± 0.5	1.4	8.0+ 0.3 - 0.3	4.0	1.75
	F	D	D1	Po	P1	Ao	Bo	Ko	t
	3.5 ± 0.05	1.5 +0.1	0.1MIN	4.0	2.0 ± 0.05	3.1	3.0	1.3	0.2±0.03

## Cover Tape Dimensions

Application	Carrier Width	Cover Tape Width	Devices Per Reel
SOT 23-3L	8	5.3	3000